



GEFÖRDERT VOM

Bundesministerium
für Bildung
und Forschung

IT4Innovations
national
supercomputing
center

Intel MIC Programming Workshop @ IT4I Preliminary Schedule

Tuesday, February 7, 2017

- 09:00-09:30 Registration
- 09:30-09:45 Welcome
- 09:45-10:30 Salomon intro
- 10:30-11:00 Coffee break
- 11:00-12:00 Overview of the Intel MIC architecture and programming models
- 12:00-13:00 Lunch break
- 13:00-13:30 Native mode programming
- 13:30-15:30 OpenMP and offloading I
- 15:30-16:00 Coffee break
- 16:00-17:00 OpenMP and offloading II
- 17:00-18:00 MKL

Wednesday, February 8, 2017

09:00-10:30 MPI

10:30-11:00 Coffee break

11:00-12:00 Vectorisation and Intel Xeon Phi performance optimisation

12:00-13:00 Lunch break

Plenum session with invited talks, Wednesday, February 8, 2017:

13:00-13:45 Jan Zapletal (IT4Innovations): Boundary element quadrature schemes for multi- and many-core architectures

13:45-14:15 Jiri Jaros (VUT Brno): Acceleration of the k-Wave toolbox on Xeon Phi

14:15-15:00 Lukasz Szustak, Roman Wyrzykowski (TU Czestochowa): Exploring the impact of Intel MIC and Intel CPU architectures on accelerating scientific applications

15:00-15:30 Coffee break

15:30-16:15 Michal Merta (IT4Innovations): Acceleration of the ESPRESO domain decomposition library

16:15-17:00 Milan Jaros (IT4Innovations): Acceleration of Blender Cycles Render Engine using Intel® Xeon Phi™